

FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))		Attorney Docket No.: GR 00 P 1031	Applic. No. 09/760,405
		Applicant Xiaoning Nie	
		Filing Date January 12, 2001	Group Art Unit 2183

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
B50	A	5,404,469	04/04/95	Chung et al.			
B50	B	5,941,983	08/24/99	Gupta et al.			
	C						
	D						
	E				RECEIVED		
	F				FEB 20 2002		
	G				Technology Center 2100		
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
B50	J	WO 99/21088	04/29/99	WIPO			X
	K						
	L						
	M						
	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	O	
	P	

EXAMINER	DATE CONSIDERED
	1/6/04
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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	A						
	B						
	C						AUG 02 2002
	D			COPY OF PAPERS			
	E			ORIGINALLY FILED			Technology Center 2100
	F						
	G						
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

BSC	O	Dean M. Tullsen et al.: "Simultaneous Multithreading: Maximizing On-Chip Parallelism", Proc. Of 22 nd Ann. Int. Symp. Comp. Archit., 1995, pp. 392-403;
BSC	P	Lance Hammond et al.: "A Single-Chip Multiprocessor", IEEE Computer, September 1997, pp. 79-85

EXAMINER

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